## EAST Search History

Ref#	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
Ll	1	("7151567").PN.	US-PGPUB; USPAT	OR	OFF	2009/01/19 13:37
S98	1386	(348/308).CCLS	US-PGPUB; USPAT	OR	OFF	2009/01/15 16:50
<b>S</b> 99	180	\$98 and (reset near2 line) and (row near2 select\$4 near2 line)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/15 16:51
S10 0	102	\$98 and 4T	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/15 19:29
SI0 1	24	((plural\$3 or multipl \$4) near2 capacitor) with (CDS or (correlated near1 double))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 10:55
S10 2	1	(villecco.xa. or villecco.xp.) and ((multiple or plural \$4) near4 capacitor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 10:59
SI0 3	53	((plural\$3 or multipl \$4) near2 capacitor) same (CDS or (correlated near1 double))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 11:00
S10 4	7	Sl0 3 and "348"/ \$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 11:00
S10 5	6	(villecco.xa. or villecco.xp.) and ((siz \$3 or number) near4 capacitor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 11:05
S10 6	1544	((plural\$3 or multipl \$4) near2 capacitor) near4 output\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	OR	ON	2009/01/16 11:07

S10 7	76	SI0 6 and "348"/ \$ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 11:07
S10 8	17	Sl07 and (CDS or (correlated near1 double))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 11:08
S10 9	3888	348/294-309.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 15:07
S110	208	Si09 and (simultaneous\$3 near2 reset\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 15:07
S111	82	Si09 and (global near2 reset\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 15:17
S112	55	(348/211.14).CCLS	US-PGPUB; USPAT	OR	OFF	2009/01/16 16:12
S113	O	((MOS or CMOS) and semiconductor and substrate and pixel and row and column and cathode and reset\$4 and shift \$4 and power and select\$4).clm.	US-PGPUB	OR	ON	2009/01/19 09:50
S114	O	((MOS or CMOS) and semiconductor and substrate and pixel and row and column and reset\$4 and shift\$4 and power and select\$4).clm.	US-PGPUB	OR	ON	2009/01/19 09:51
S115	2	((MOS or CMOS) and semiconductor and substrate and pixel and row and column and reset\$4 and shift\$4 and select \$4).clm.	US-PGPUB	OR	ON	2009/01/19 09:51

1/19/09 2:47:44 PM C:\Documents and Settings\jvillecco\My Documents\EASI\workspaces\1110 2261.wsp